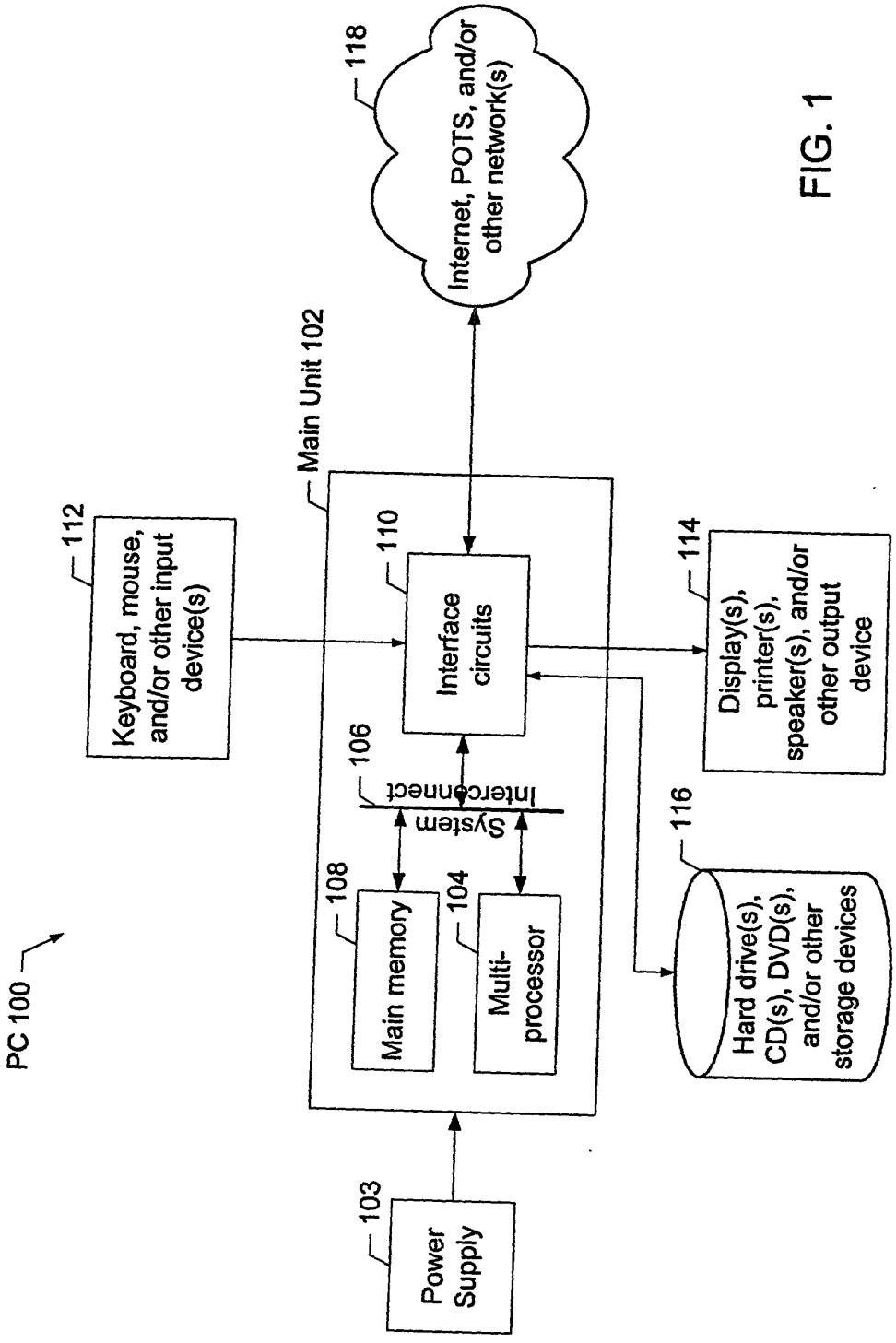
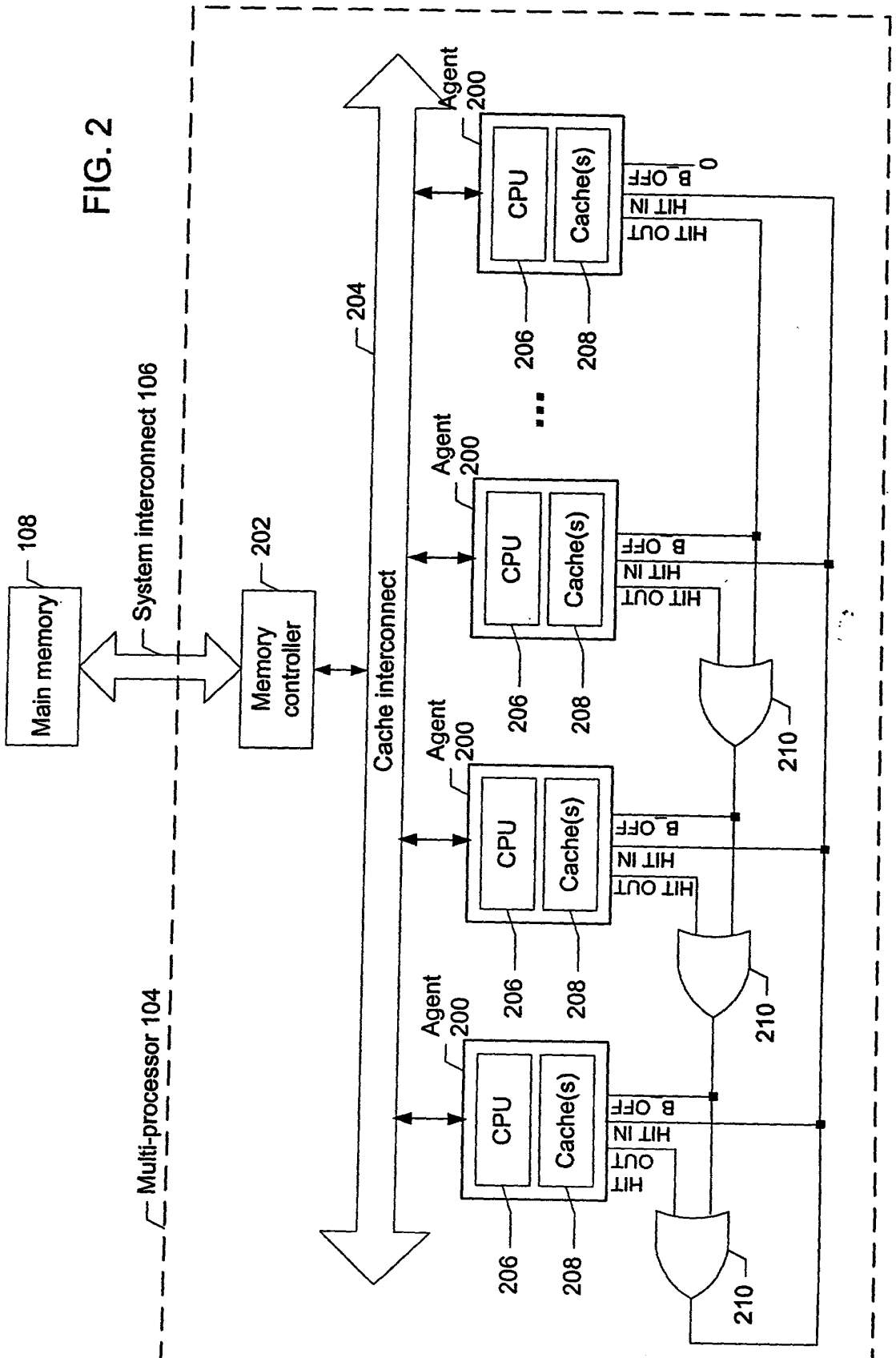
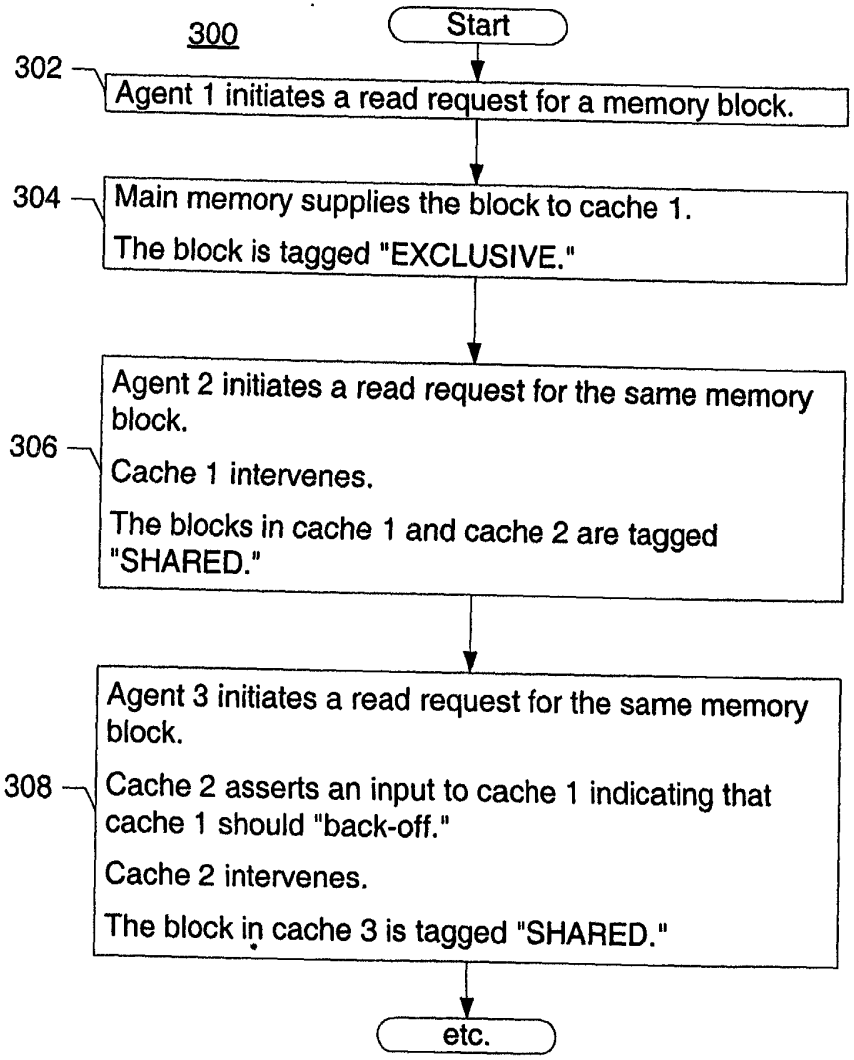

PC 100
Main Unit 102
Power Supply
103
Main memory
108
Multi-processor
104
System Interconnect
106
Interface circuits
110
Keyboard, mouse, and/or other input device(s)
112
Internet, POTS, and/or other network(s)
118
Display(s), printer(s), speaker(s), and/or other output device
114
Hard drive(s), CD(s), DVD(s), and/or other storage devices
116

FIG. 1

FIG. 2


Main memory
108
System interconnect 106
Memory controller
202
Cache interconnect
204
Multi-processor 104
Agent
200
CPU
Cache(s)
206
208
B_OFF
HIT IN
HIT OUT
210
0

2001-20-26th2001



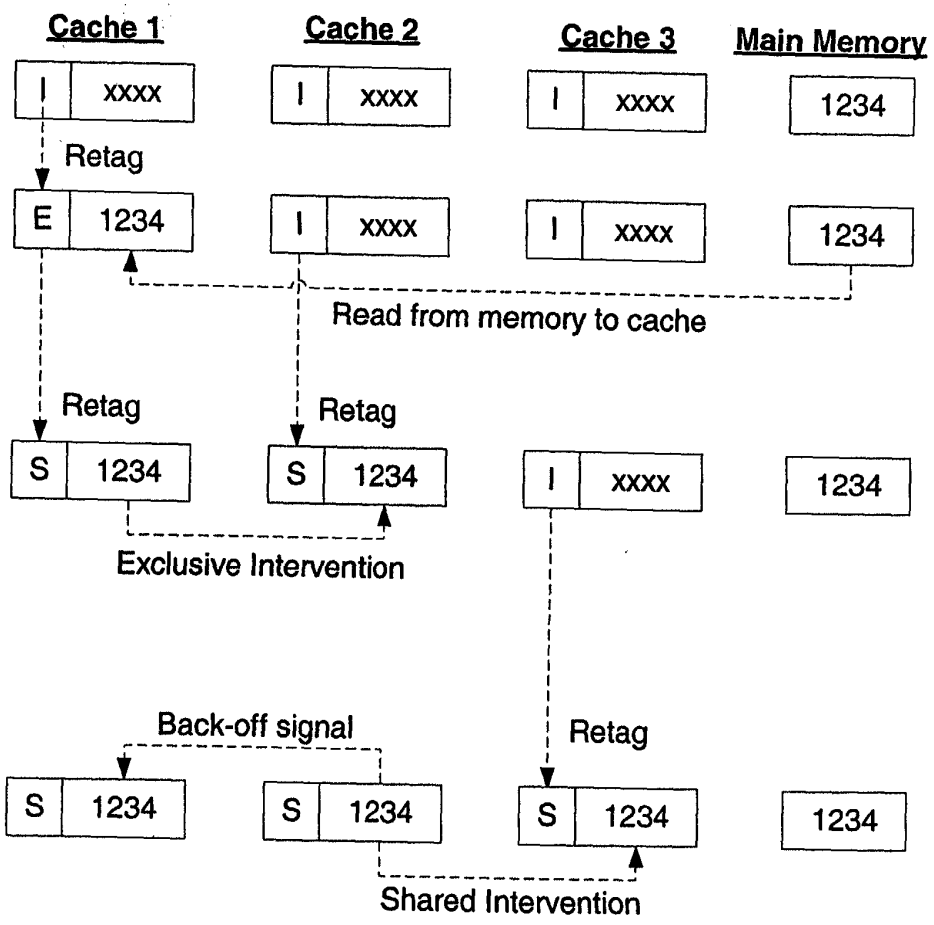
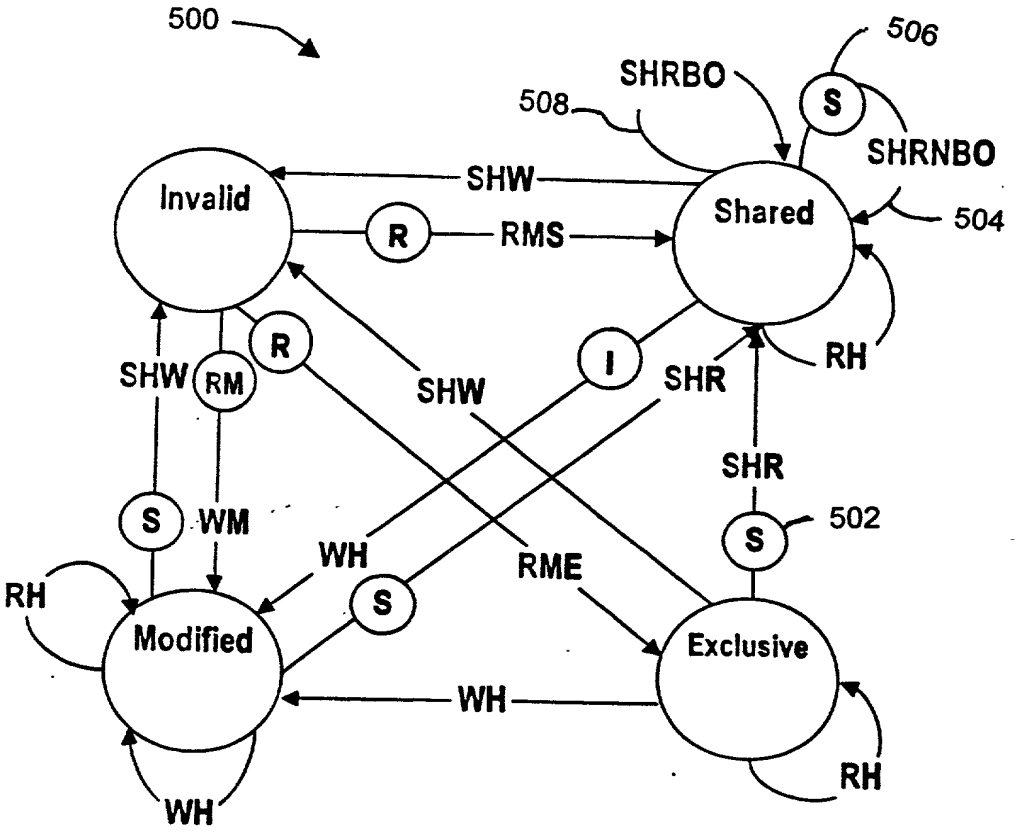


FIG. 3



I - Invalidate
R - Cache line Fill
S - Snoop Push
RM - Read With Intent to Modify

RH - Read Hit
RMS - Read Miss, Shared
RME - Read Miss, Exclusive
WH - Write Hit
WM - Write Miss
SHR - Snoop Hit on a Read
SHRBO – Snoop Hit on a Read with Back Off
SHRNBO – Snoop Hit on a Read with no Back Off
SHW - Snoop Hit on a Write or Read With Intent to Modify

FIG. 4